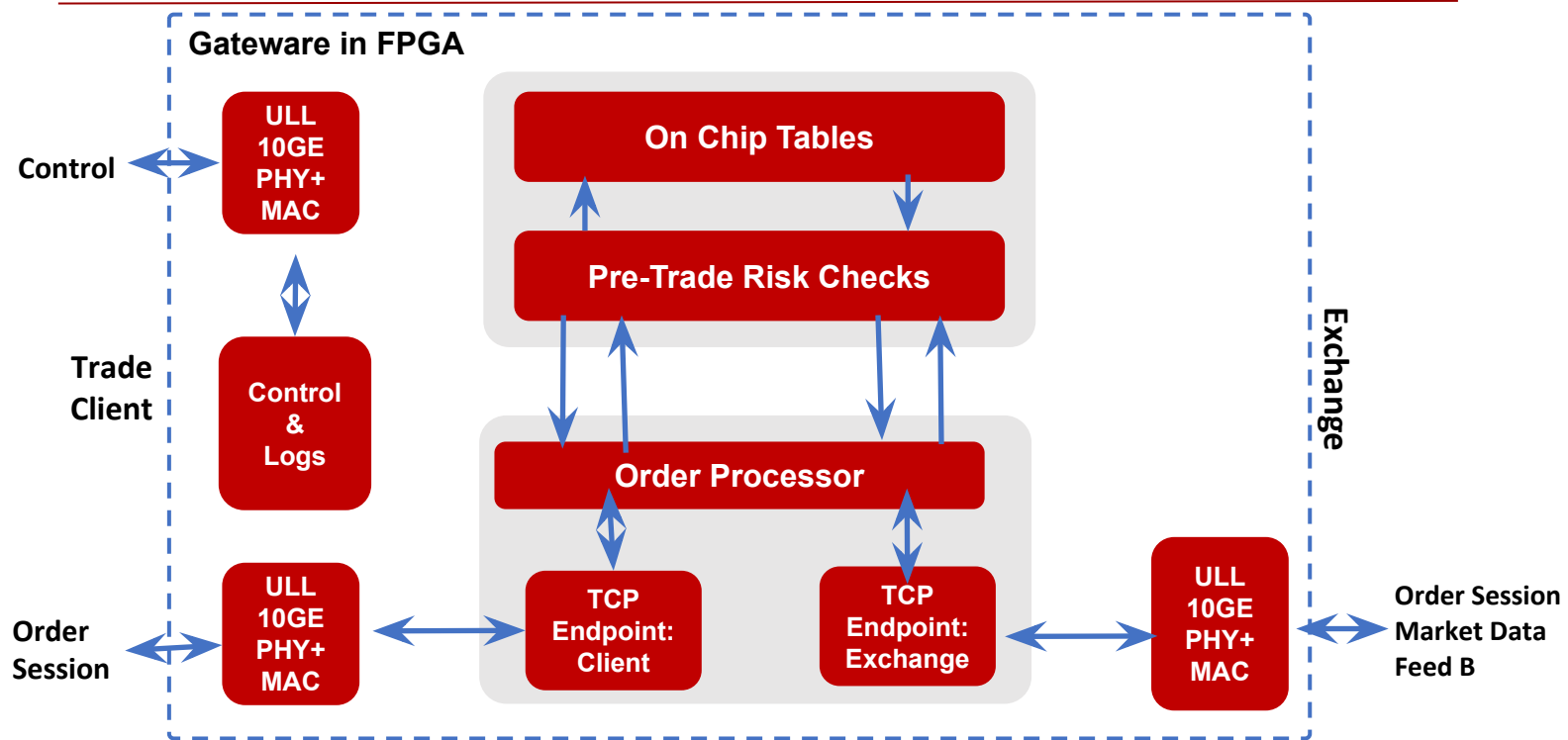


FPGA Pre-Trade Risk Check (PTRC)

Pre-Trade Risk Check System (PTRC) Diagram



Description

Algo-Logic Systems delivers an industry leading Pre-Trade Risk Check (PTRC) solution that verifies Security and Exchange Commission Rule 15c3-5 compliance for US equities and CFTC regulations for futures. With sub-microsecond latency that is consistently lower than software-based systems.

Algo-Logic PTRC is:

- The newest addition to Algo-Logic's Ultra-low-latency systems for accelerated finance.
- Offers deep sub-microsecond latency from Client Order Received to Valid Order Sent to Exchange.
- Is easy to use and has a secure Graphical User Interface to control parameters of Risk Checks.
- Provides Standard Rule 15c3-5 Risk Checks that are available immediately.
- Produces custom, Broker specific Risk Checks quickly under a Professional Services Agreement.
- Is significantly faster than software-only solutions.
- Yields a deterministic performance with deep sub-microsecond latency.

FPGA Pre-Trade Risk Check (PTRC)

Examples of Equity Order Risk Checks	Description
Maximum Order Quantity	Total Qty of shares > Max Order Qty
Maximum Order Value	Total Value of Order > Max Order Value
Max Range Share Price - Last Order	Order Share Price is greater than x% or less than x% of Last Order
Max Share Price - NBBO/BBO	Max Order Share Price > Current NBBO/BBO
Min Share Price - NBBO/BBO	Min Order Share Price < Current NBBO/BBO
Allowed to Short	Order Symbol is not on Easy to Borrow List
Restricted Stock List	Order Symbol is on the Restricted Stock List
Throughput Limit - Max Order/sec	Order rate > Max Orders/second
Breach Risk Max Risk Check fails	Risk Check Failures Greater than Max Risk Check Failures allowed

Example Configuration 1: Nasdaq PTRCs on ALVEO U200 FPGA Card

- Preconfigured Dell R740 Server
 - Chassis with GPU Risers
 - Configuration with ALVEO U200
- PTRC License from Algo-Logic
 - FPGA bitstream Gateway
 - Graphical User Interface



Example Configuration 2: CME Risk Checks on Cisco/Exablaze V5P FPGA Card

- Preconfigured HP Server
 - Chassis with GPU Risers
 - Configuration with ALVEO U200
- PTRC License from Algo-Logic
 - FPGA bitstream Gateway
 - Graphical User Interface



Supported Exchanges

